

# STM32MP25XXAJ\_DDR4

## Schematics not complete, only for layout example

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U\_STM32MP25XXAJ\_DDR4\_TOP  
STM32MP25XXAJ\_DDR4\_TOP.SchDoc



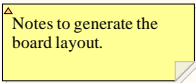
### Note

### Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.



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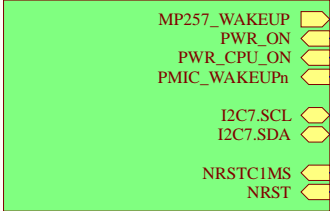
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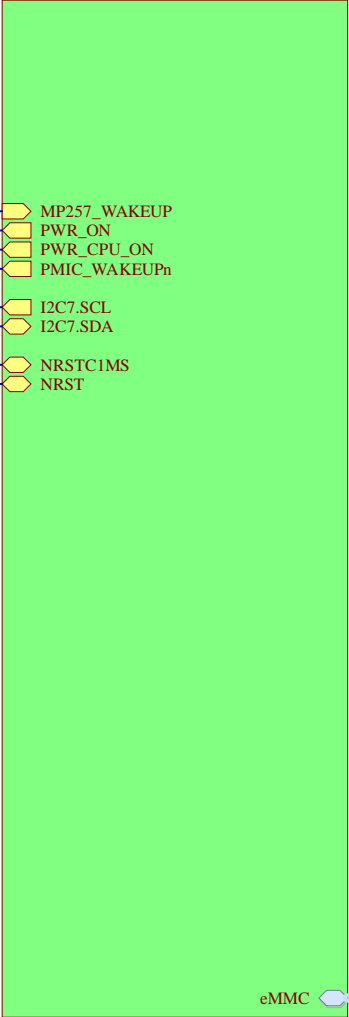


U\_STM32MP25XXAJ Power  
STM32MP25XXAJ Power.SchDoc

U\_PMIC  
PMIC.SchDoc



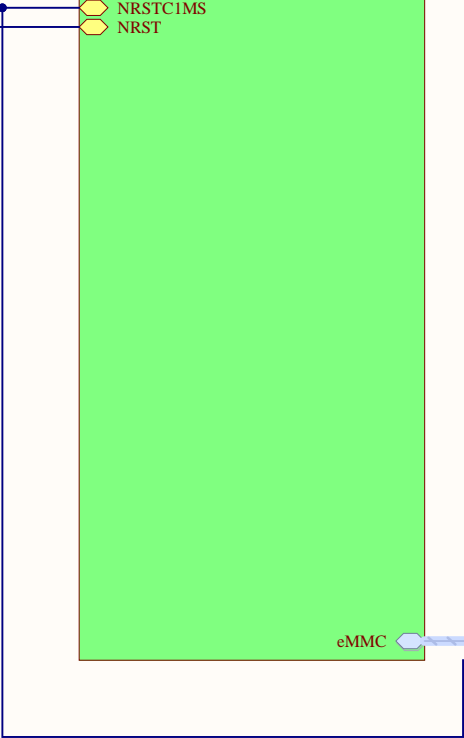
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STM32MP25XXAJ I\_Os.SchDoc

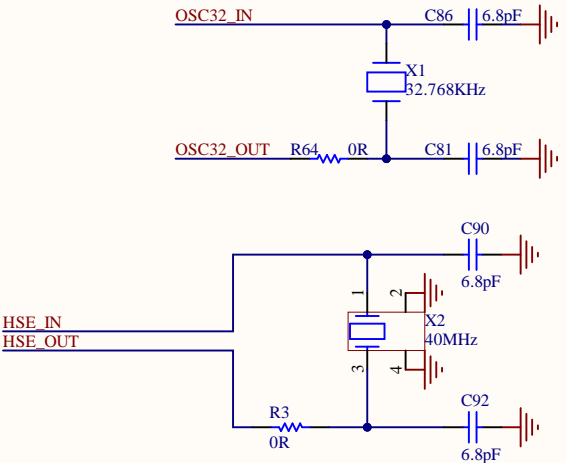
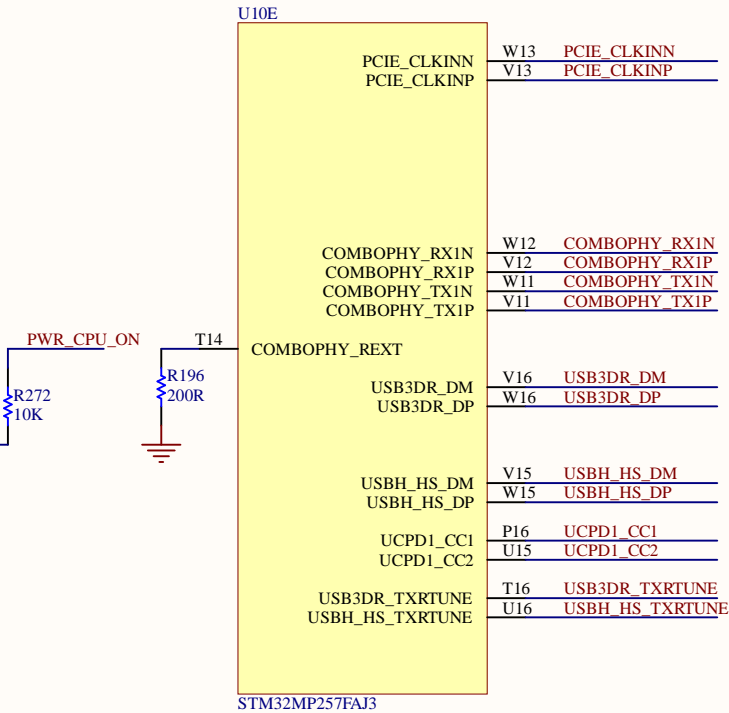
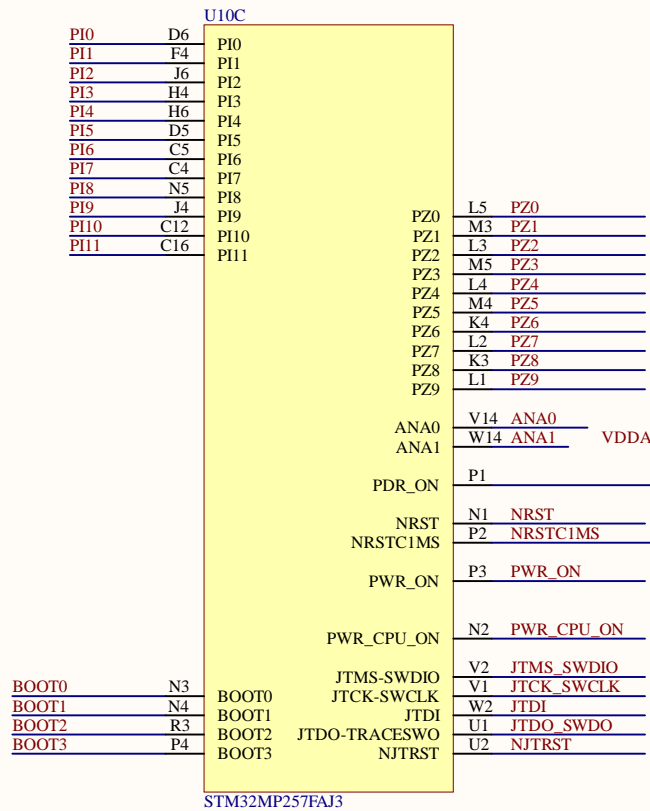
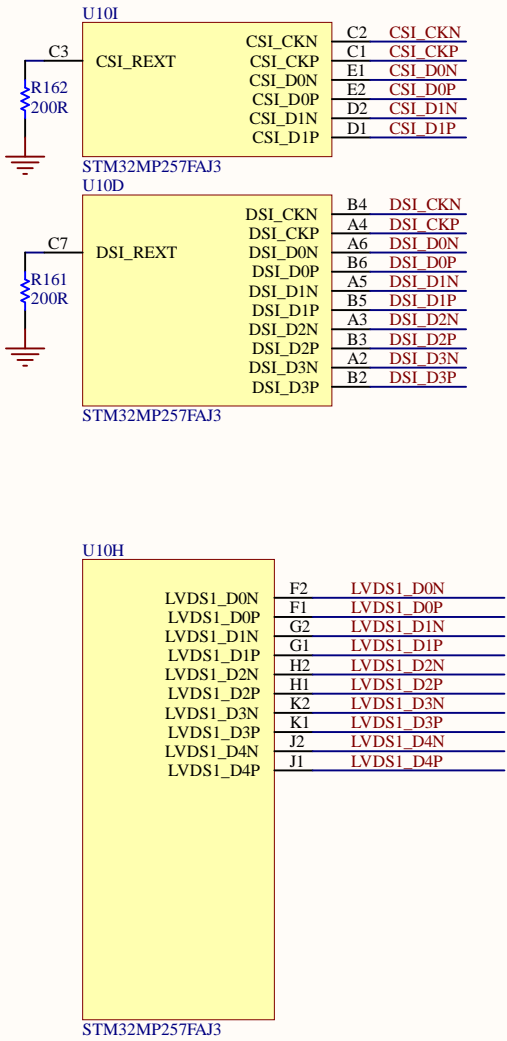
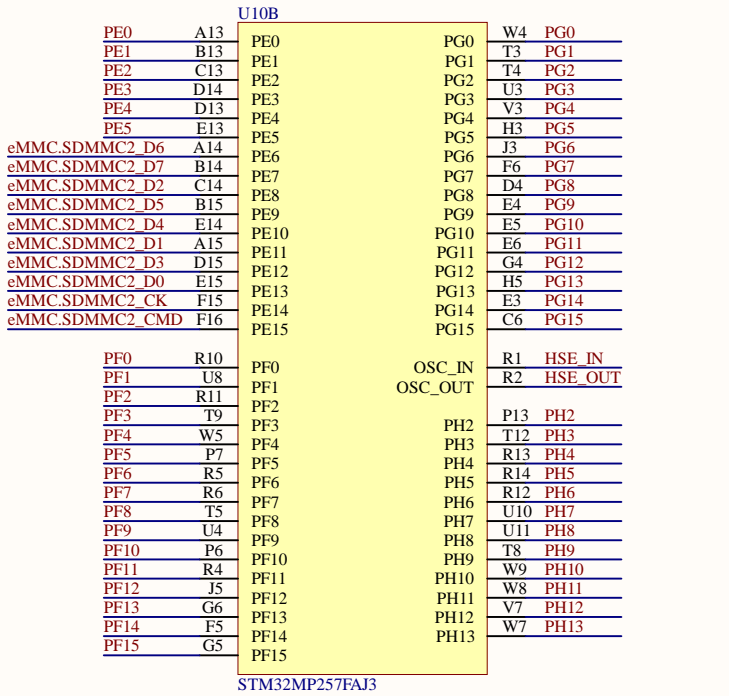
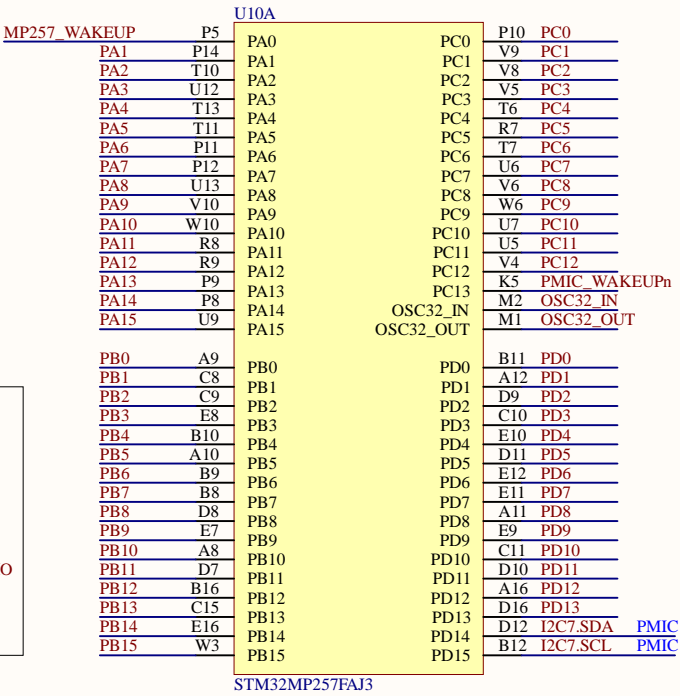
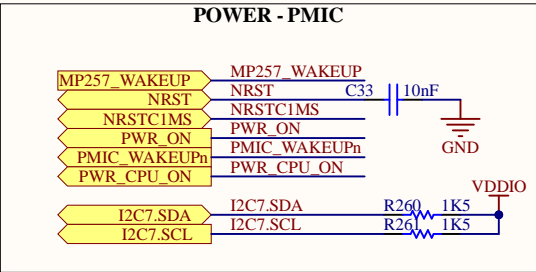
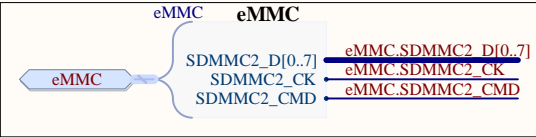


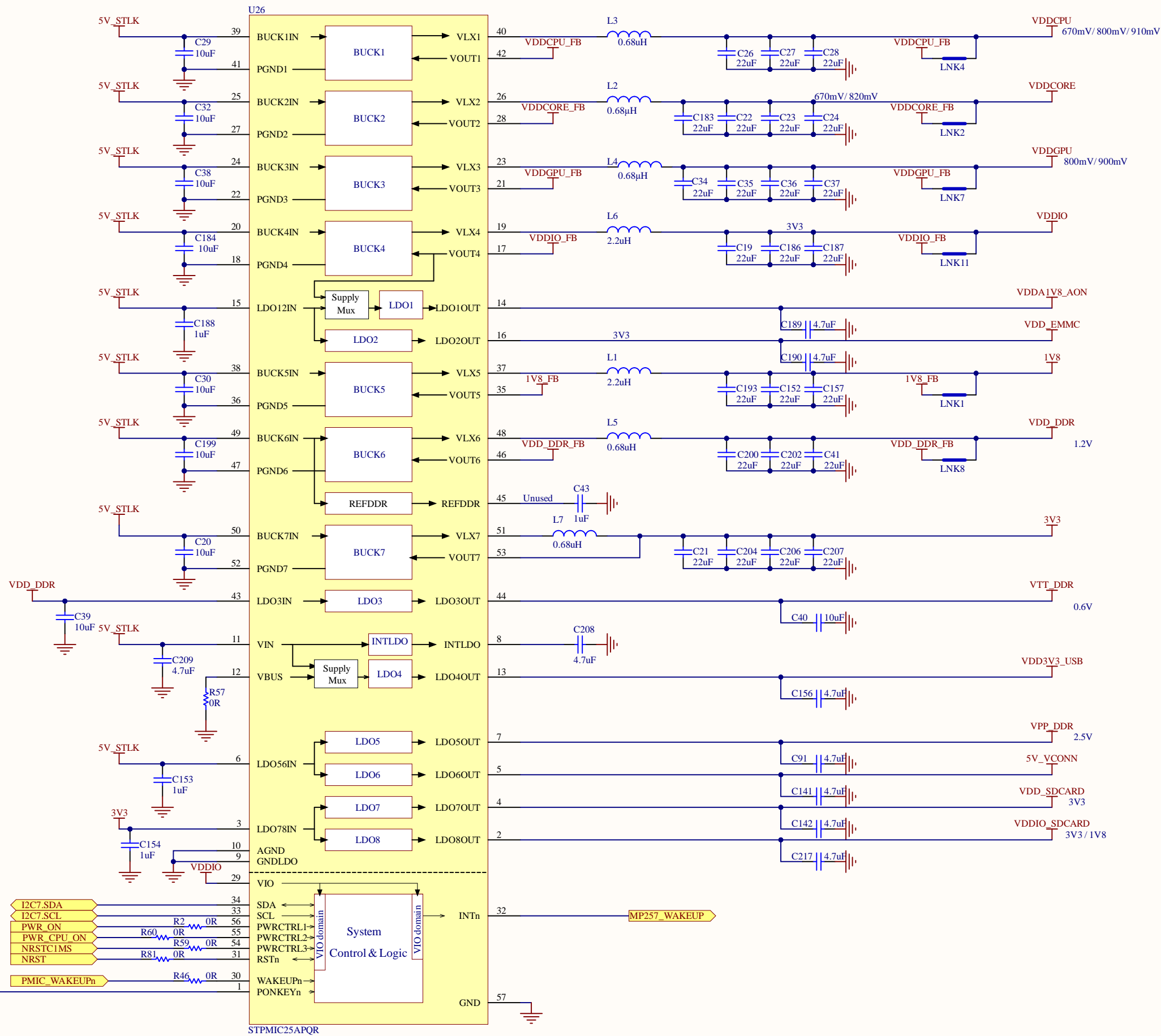
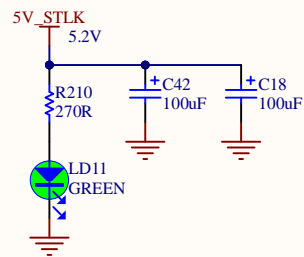
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DDR4\_16bits.SchDoc



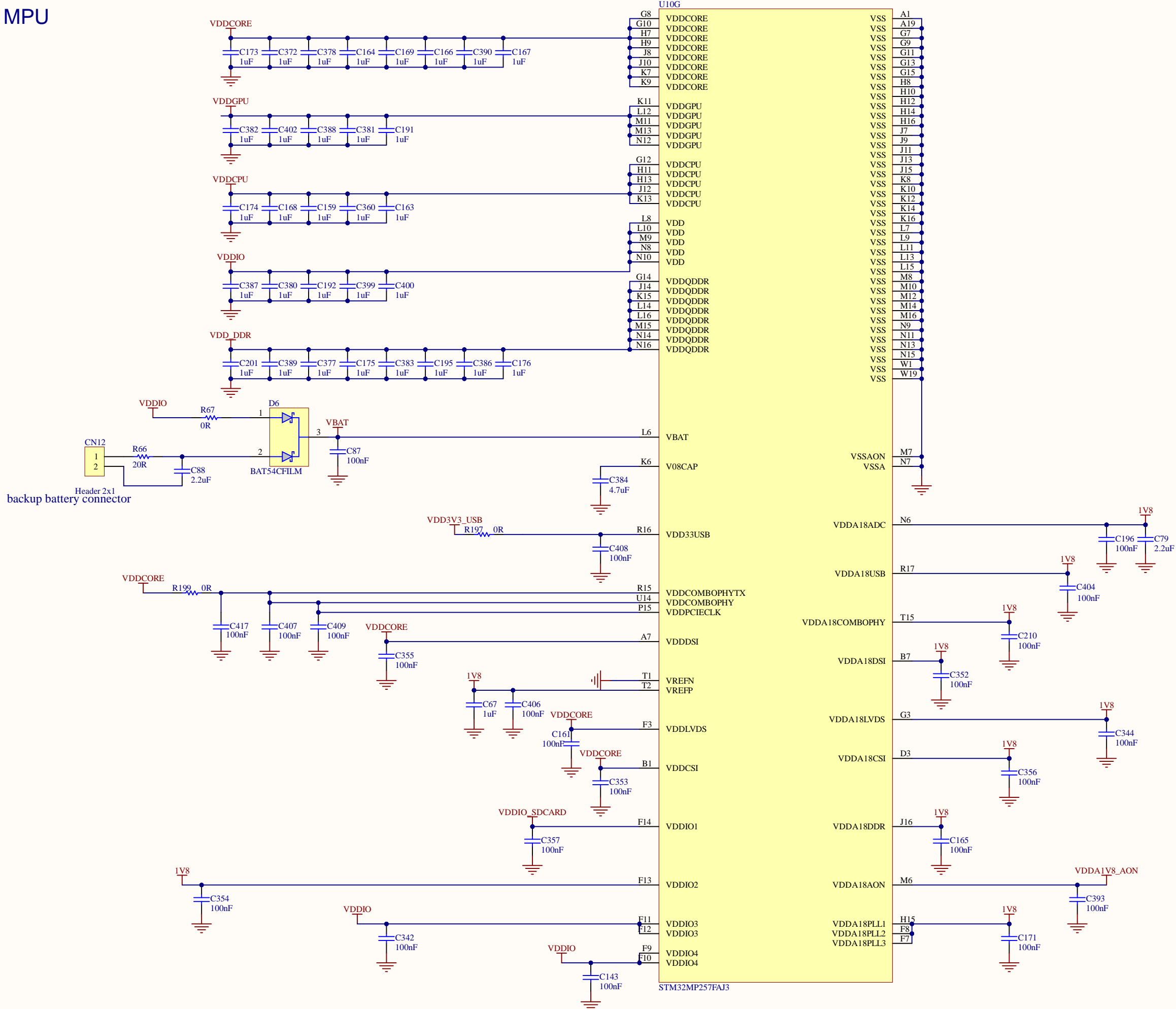
U\_eMMC  
eMMC.SchDoc








## Power MPU



Title: <b>STM32MP25XXAJ Power</b>			
Project: <b>STM32MP25XXAJ_DDR4</b>			
Variant: <b>[No Variants]</b>			
Revision: <b>B-01</b>		Reference:	
Size: <b>A3</b>	Date: <b>25-APR-25</b>	Sheet: <b>5</b> of <b>7</b>	

